

FIG. 1

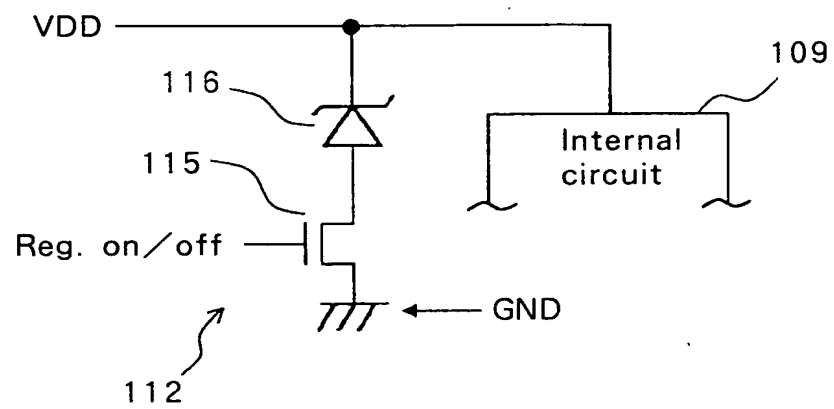


FIG. 3

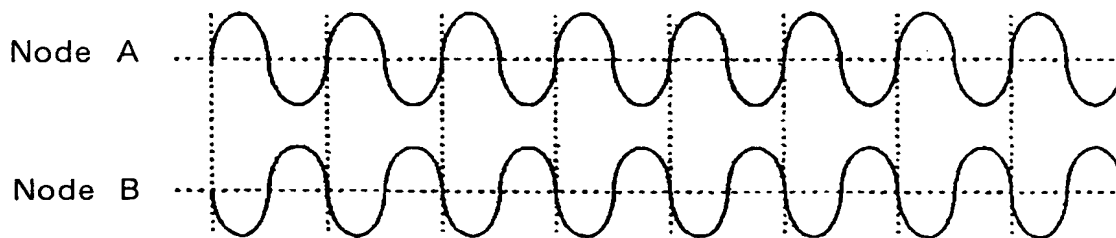


FIG. 4

Node A

Node B

Internal
VDD

POR

RESET_B

ISO/RF_B

Reg. on/off

F/F②

DET_WAVE

FIG. 5

Undefined

VDD

RESET_B

POR

ISO/RF_B

Reg. on/off

F/F②

DET_WAVE

FIG. 6

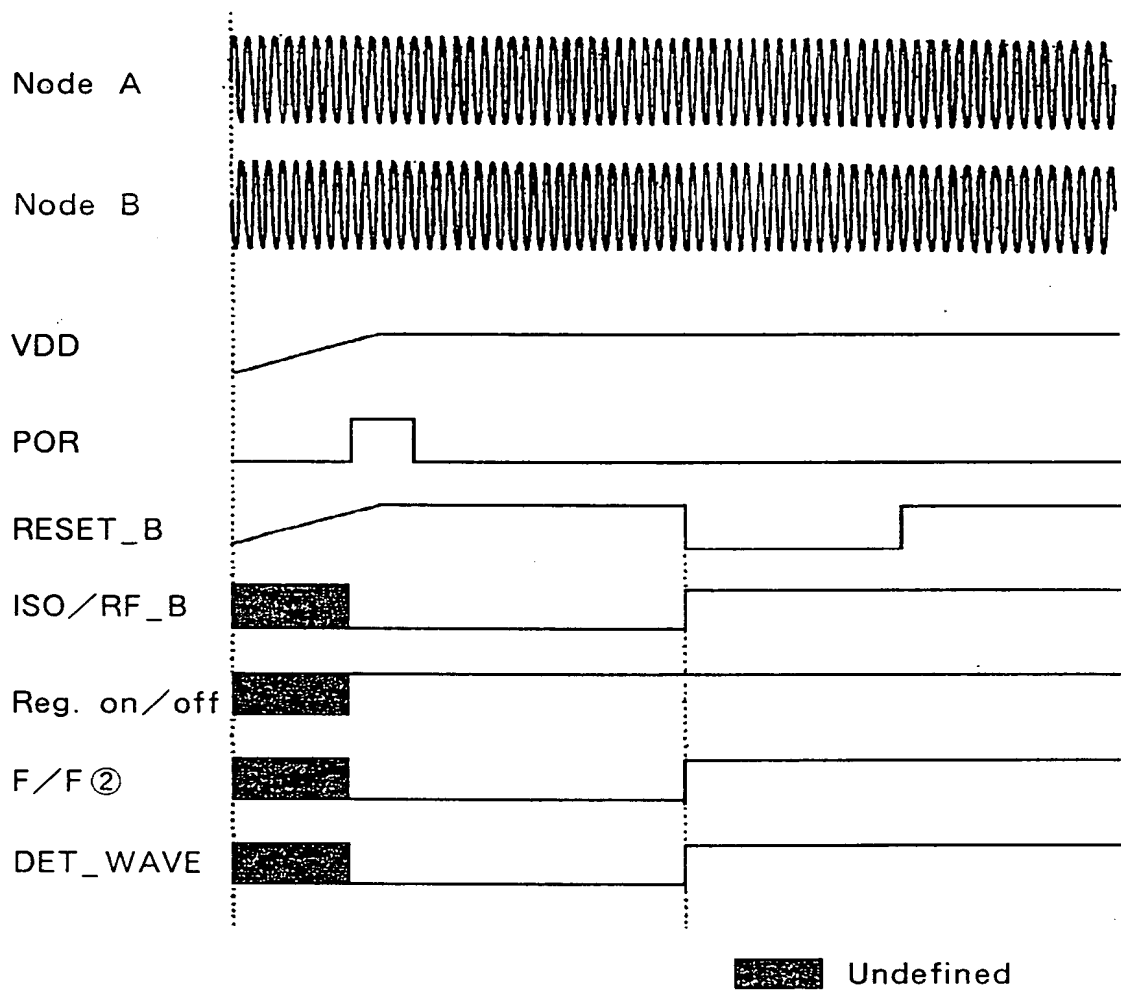


FIG. 7

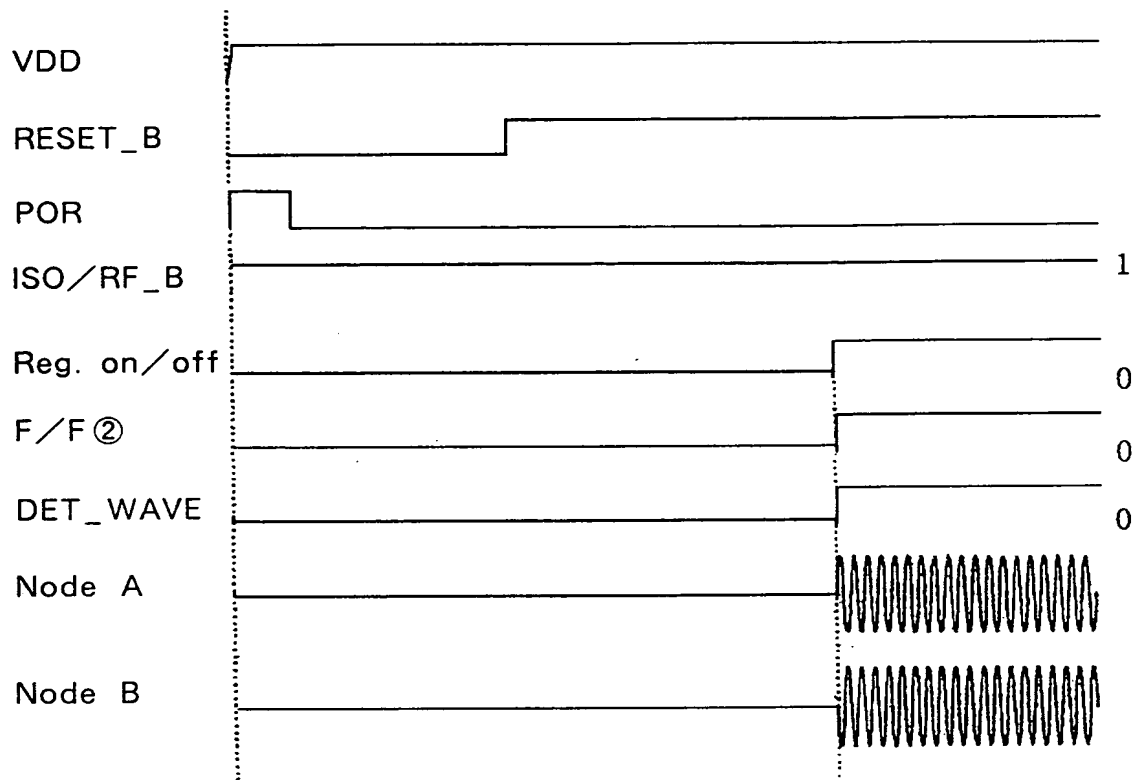


FIG. 8